

2SK2700

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance : $R_{DS(ON)} = 3.7 \Omega$ (typ.)
- High forward transfer admittance : $|Y_{fs}| = 2.6 S$ (typ.)
- Low leakage current : $I_{DSS} = 100 \mu A$ (max) ($V_{DS} = 720 V$)
- Enhancement mode : $V_{th} = 2.0 \sim 4.0 V$ ($V_{DS} = 10 V, I_D = 1 mA$)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	900	V
Drain-gate voltage ($R_{GS} = 20 k\Omega$)	V_{DGR}	900	V
Gate-source voltage	V_{GSS}	± 30	V
Drain current	DC (Note 1)	I_D	3 A
	Pulse (Note 1)	I_{DP}	9 A
Drain power dissipation ($T_c = 25^\circ C$)	P_D	40	W
Single pulse avalanche energy (Note 2)	E_{AS}	295	mJ
Avalanche current	I_{AR}	3	A
Repetitive avalanche energy (Note 3)	E_{AR}	4	mJ
Channel temperature	T_{ch}	150	°C
Storage temperature range	T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th(ch-c)}$	3.125	°C / W
Thermal resistance, channel to ambient	$R_{th(ch-a)}$	62.5	°C / W

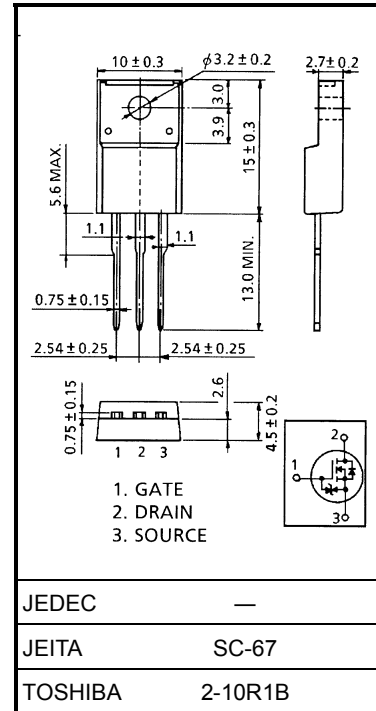
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 V, T_{ch} = 25^\circ C$ (initial), $L = 60.0 mH, R_G = 25 \Omega, I_{AR} = 3 A$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.
Please handle with caution.

Unit: mm



Weight: 1.9 g (typ.)

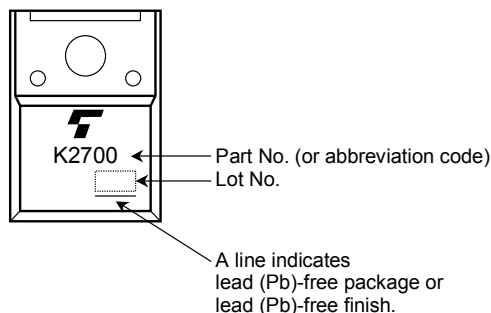
Electrical Characteristics (Ta = 25°C)

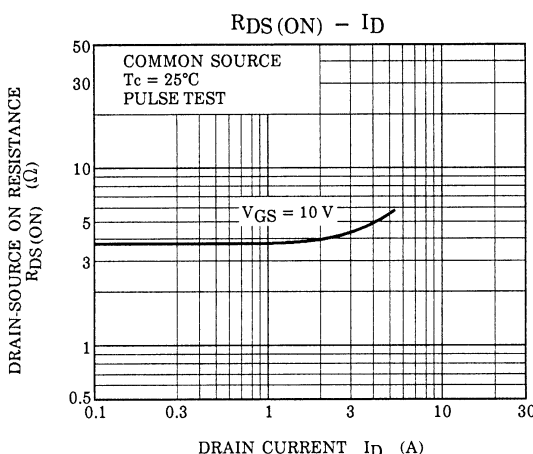
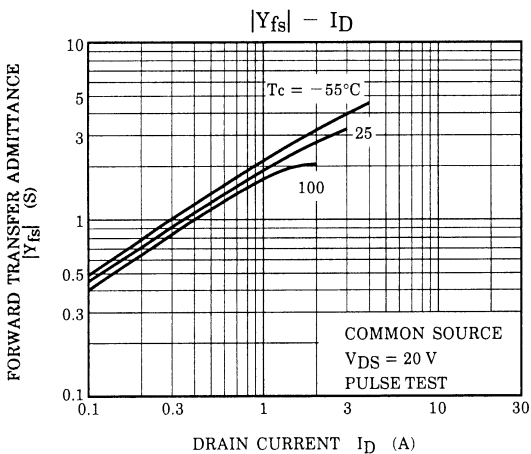
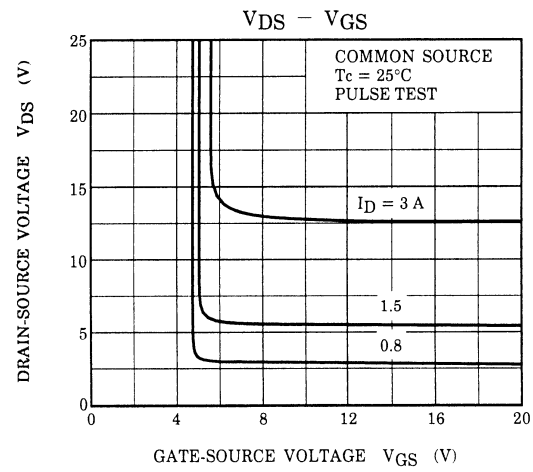
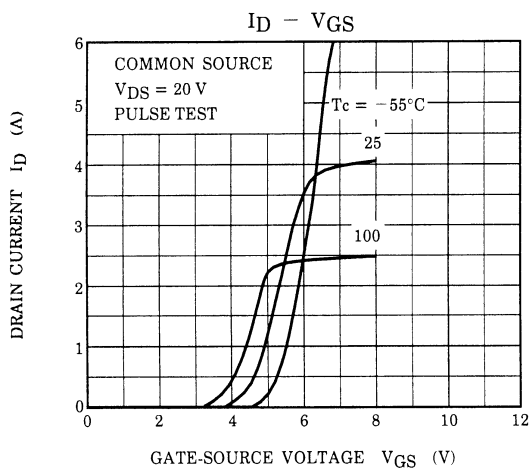
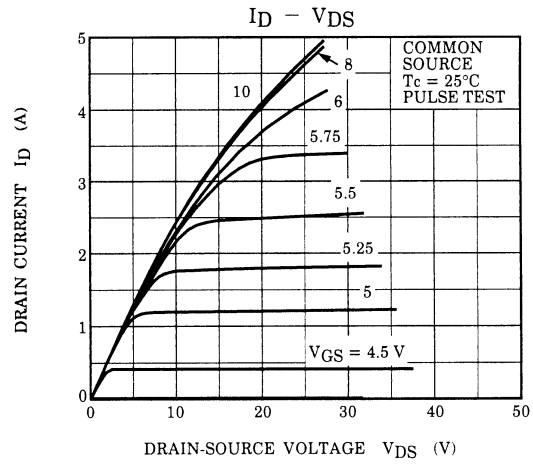
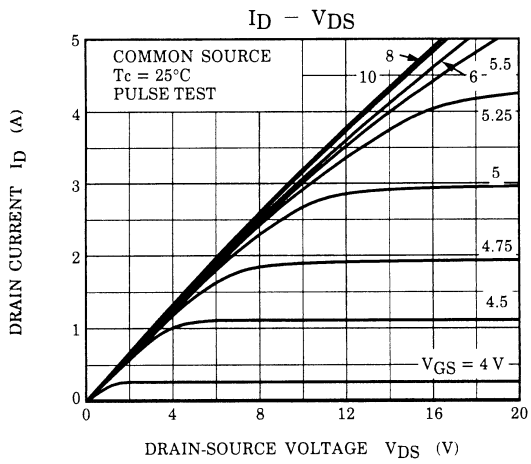
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	$V_{GS} = \pm 30\text{ V}, V_{DS} = 0\text{ V}$	—	—	± 10	μA
Gate–source breakdown voltage		$V_{(BR)GSS}$	$I_G = \pm 10\ \mu\text{A}, V_{DS} = 0\text{ V}$	± 30	—	—	V
Drain cut–off current		I_{DSS}	$V_{DS} = 720\text{ V}, V_{GS} = 0\text{ V}$	—	—	100	μA
Drain–source breakdown voltage		$V_{(BR)DSS}$	$I_D = 10\text{ mA}, V_{GS} = 0\text{ V}$	900	—	—	V
Gate threshold voltage		V_{th}	$V_{DS} = 10\text{ V}, I_D = 1\text{ mA}$	2.0	—	4.0	V
Drain–source ON resistance		$R_{DS(ON)}$	$V_{GS} = 10\text{ V}, I_D = 1.5\text{ A}$	—	3.7	4.3	Ω
Forward transfer admittance		$ Y_{fs} $	$V_{DS} = 20\text{ V}, I_D = 1.5\text{ A}$	0.65	2.6	—	S
Input capacitance		C_{iss}	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	—	750	—	pF
Reverse transfer capacitance		C_{rss}		—	10	—	
Output capacitance		C_{oss}		—	70	—	
Switching time	Rise time	t_r		—	15	—	ns
	Turn–on time	t_{on}		—	55	—	
	Fall time	t_f		—	30	—	
	Turn–off time	t_{off}		—	110	—	
Total gate charge (gate–source plus gate–drain)		Q_g	$V_{DD} \approx 400\text{ V}, V_{GS} = 10\text{ V}, I_D = 3\text{ A}$	—	25	—	nC
Gate–source charge		Q_{gs}		—	13	—	
Gate–drain (“miller”) Charge		Q_{gd}		—	12	—	

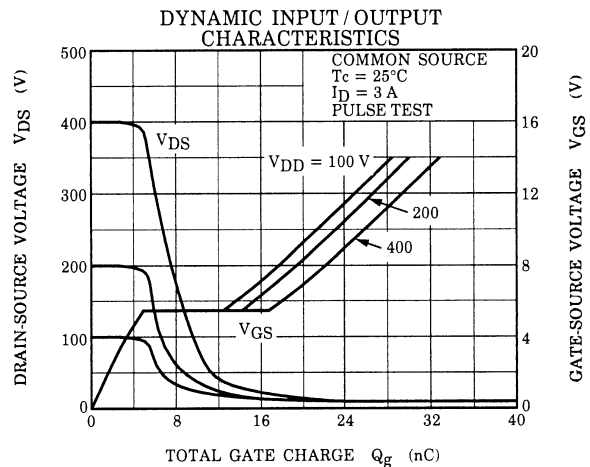
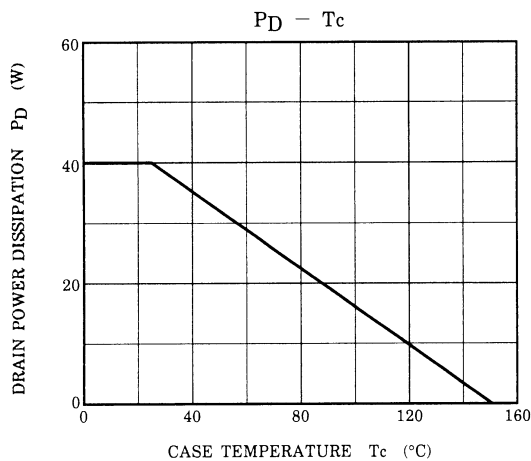
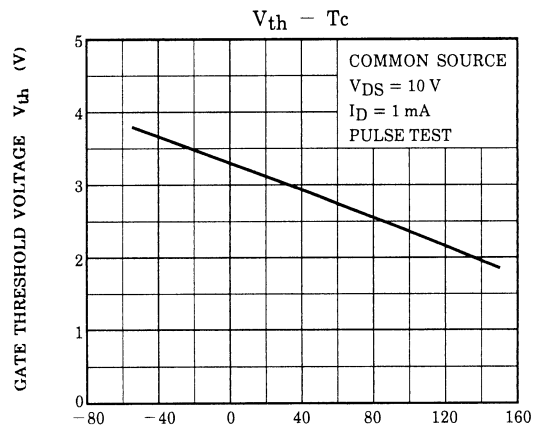
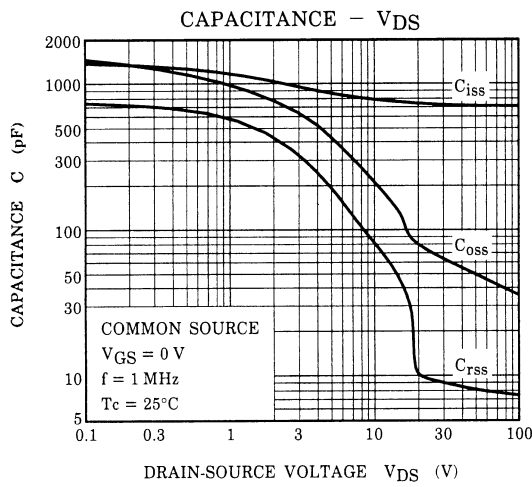
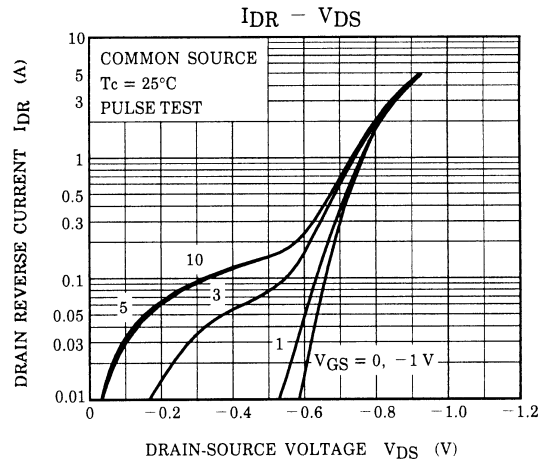
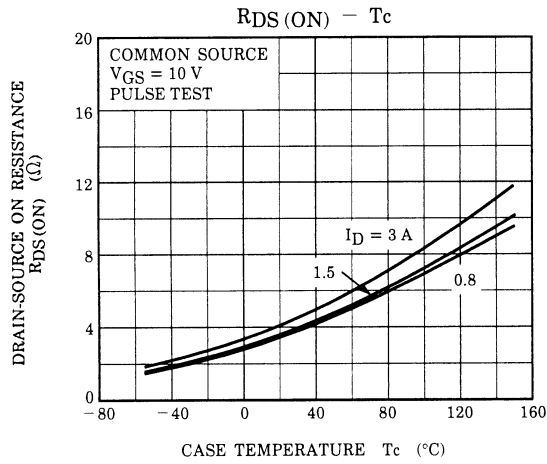
Source–Drain Ratings and Characteristics (Ta = 25°C)

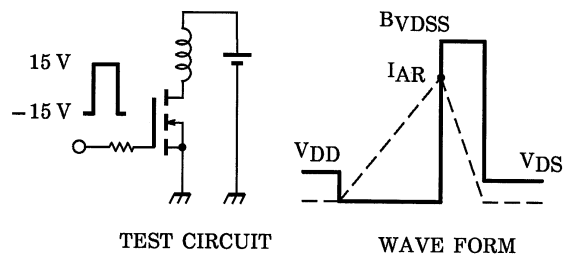
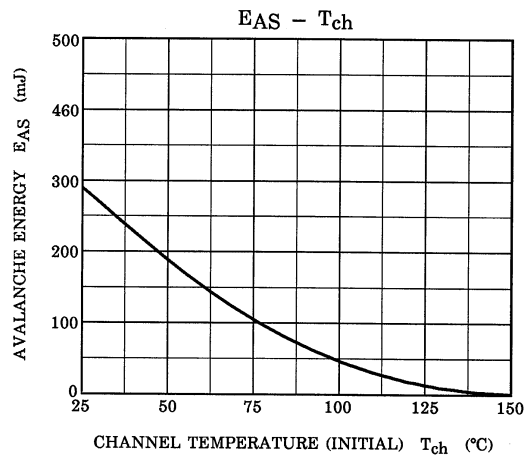
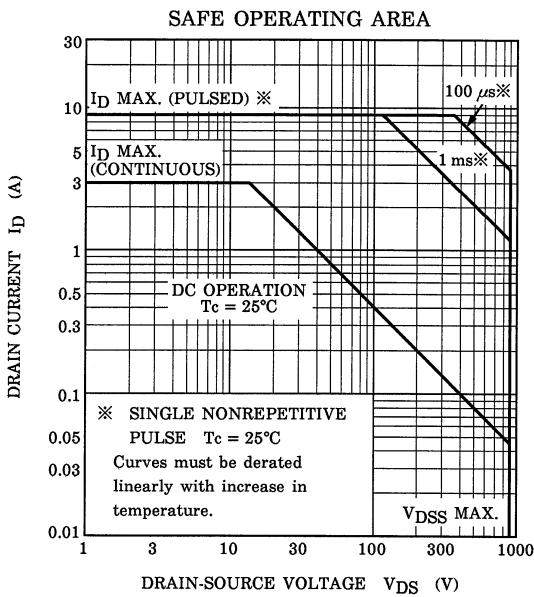
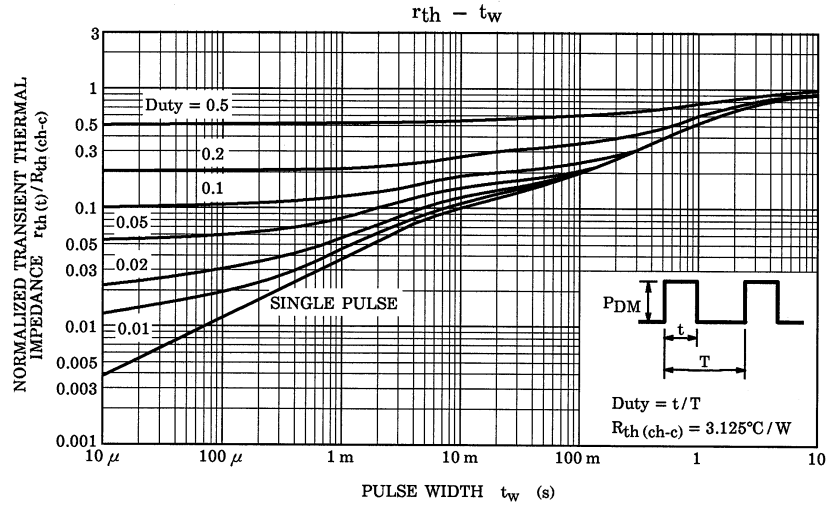
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)		I_{DR}	—	—	—	3	A
Pulse drain reverse current (Note 1)		I_{DRP}	—	—	—	9	A
Forward voltage (diode)		V_{DSF}	$I_{DR} = 3\text{ A}, V_{GS} = 0\text{ V}$	—	—	–1.9	V
Reverse recovery time		t_{rr}	$I_{DR} = 3\text{ A}, V_{GS} = 0\text{ V}$	—	1100	—	ns
Reverse recovery charge		Q_{rr}	$dI_{DR} / dt = 100\text{ A} / \mu\text{s}$	—	7.2	—	μC

Marking









$R_G = 25 \Omega$
 $V_{DD} = 90 V, L = 60 mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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